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#### 2 A peak power reduction scheme with phase-control of clustered parity-carriers for a systematic block-coded OFDM signal

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**6 Generic ILP-based approaches for dynamically reconfigurable FPGA partitioning**

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**10 TDMA/FDMA PCN systems: an advanced channel borrowing strategy**

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*Haraszti, T.P.;*

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*McGarty, T.;*

Automatic Control, IEEE Transactions on , Volume: 20 , Issue: 5 , Oct 1975

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3 **A single-chip programmable platform based on a multithreaded processor and configurable logic clusters**

*Young-Don Bae; Seong-Il Park; In-Cheol Park;*

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4 **Network configurations for seamless support of CDMA soft handoffs between cell clusters**

*Cheung, B.H.; Leung, V.C.M.;*

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5 **Error control coding**

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#### 6 **Group technology and cellular manufacturing**

*Heragu, S.S.;*

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#### 7 **NETRA: a hierarchical and partitionable architecture for computer vision systems**

*Choudhary, A.N.; Patel, J.H.; Ahuja, N.;*

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#### 8 **Performance analysis of a duplex multicast switch**

*Kim, C.-K.;*

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#### 9 **Optimized fuzzy transformation for image deblocking**

*Yao Nie; Barner, K.E.;*

Multimedia and Expo, 2003. ICME '03. Proceedings. 2003 International Conference on , Volume: 1 , 6-9 July 2003

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*Hariharakrishnan, K.; Schonfeld, D.; Raffy, P.; Yassa, F.;*

Image Processing, 2003. Proceedings. 2003 International Conference on , Volume: 3 , 14-17 Sept. 2003

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*Hariharakrishnan, K.; Schonfeld, D.; Raffy, P.; Yassa, F.;*

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#### 12 **A peak power reduction scheme with phase-control of clustered parity-carriers for a systematic block-coded OFDM signal**

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*Kluge, J.; Fischer, M.; Bruning, U.; Rzymianowicz, L.; Schulz, P.; Waack, M.;*  
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*Beming, P.; Frodigh, M.;*  
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*Dang, K.; Desanti, T.; Yoo, B.; Khawaja, Y.;*  
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*Naghshineh, M.; Acampora, A.S.;*  
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*Granger, E.; Blaquiere, Y.; Savaria, Y.; Cantin, M.-A.; Lavoie, P.;*  
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21 **A gain-shape vector quantizer for image coding**

*Lee, H.; Lee, D.;*

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22 **Leveraging block decisions and aggregation in the ShareStreams QoS architecture**

*Krishnamurthy, R.; Yalamanchili, S.; Schwan, K.; West, R.;*

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23 **Control constrained resource partitioning for complex SoCs [intra-chip wireless interconnects]**

*Zhao, D.; Upadhyaya, S.; Margala, M.;*

Defect and Fault Tolerance in VLSI Systems, 2003. Proceedings. 18th IEEE  
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*Qingsheng Zhu; Hongfu Wu; Qian Wang; Zhengyu Zhu;*

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Rapid System Prototyping, 1995. Proceedings., Sixth IEEE International Workshop on , 7-9 June 1995

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